

Description

The LM4606 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

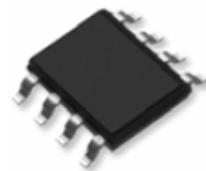
General Features

- $V_{DS} = 30V$ $I_D = 6.2A$
- $R_{DS(ON)} < 25m\Omega$ @ $V_{GS}=10V$ (Typ.18m Ω)
- $V_{DS} = -30V$ $I_D = -4.8A$
- $R_{DS(ON)} < 60m\Omega$ @ $V_{GS}=-10V$ (Typ.48m Ω)

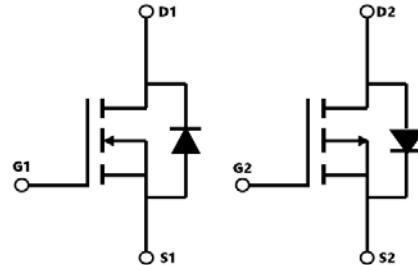
Application

- Lithium battery protection
- Wireless impact
- Mobile phone fast charging

Dimensions SOP-8



Pin Configuration



Package Marking and Ordering Information

Device	Device Marking	Device Package	Reel Size	Tape width	Quantity
LM4606	AP4606B	SOP-8	Ø330mm	12mm	3000 units

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	N-Ch	P-Ch	Units
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_A=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V ¹	6.2	-4.8	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, V_{GS} @ 10V ¹	5	-3.8	A
I_{DM}	Pulsed Drain Current ²	24	-24	A
EAS	Single Pulse Avalanche Energy ³	26.6	37	mJ
I_{AS}	Avalanche Current	12.7	15	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ⁴	1.5	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150		°C
T_J	Operating Junction Temperature Range	-55 to 150		°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	85		°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	60		°C/W

N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	30	31.5	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.023	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=5\text{A}$	---	18	25	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=3\text{A}$	---	24	40	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.0	1.6	2.5	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	-4.2	---	$\text{mV}/^\circ\text{C}$
I_{DS}	Drain-Source Leakage Current	$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	nA
		$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=6\text{A}$	---	5.8	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.3	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_D=6\text{A}$	---	5	---	nC
Q_{gs}	Gate-Source Charge		---	1.11	---	
Q_{gd}	Gate-Drain Charge		---	2.61	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=12\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.3\Omega$ $I_D=6\text{A}$	---	7.7	---	ns
T_r	Rise Time		---	46	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	11	---	
T_f	Fall Time		---	3.6	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	416	---	pF
C_{oss}	Output Capacitance		---	62	---	
C_{rss}	Reverse Transfer Capacitance		---	51	---	
I_s	Continuous Source Current ^{1,6}	$V_G=V_D=0\text{V}$, Force Current	---	---	6.2	A
I_{SM}	Pulsed Source Current ^{2,6}		---	---	24	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3、The power dissipation is limited by 150°C junction temperature
- 4、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-30	-32	---	V
$\Delta BVDSS/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	---	-0.02	---	$\text{V}/^\circ\text{C}$
RDS(ON)	Static Drain-Source On-Resistance ²	$V_{GS}=-10\text{V}$, $I_D=-4.1\text{A}$	---	42	55	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-3.5\text{A}$	---	68	85	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D = -250\mu\text{A}$	-1.0	-1.7	-2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	4.32	---	$\text{mV}/^\circ\text{C}$
IDSS	Drain-Source Leakage Current	$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	uA
		$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	-5	
IGSS	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA
gfs	Forward Transconductance	$V_{DS}=-5\text{V}$, $I_D=-3\text{A}$	---	4.7	---	S
R _g	Gate Resistance	$V_{DS}=0\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	24	---	Ω
Q _g	Total Gate Charge (-4.5V)	$V_{DS}=-20\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-5\text{A}$	---	5.22	---	nC
Q _{gs}	Gate-Source Charge		---	1.25	---	
Q _{gd}	Gate-Drain Charge		---	2.3	---	
Td(on)	Turn-On Delay Time	$V_{DD}=-15\text{V}$, $V_{GS}=-10\text{V}$, $R_G=3.3\Omega$ $I_D=-1\text{A}$	---	18.4	---	ns
T _r	Rise Time		---	11.4	---	
Td(off)	Turn-Off Delay Time		---	39.4	---	
T _f	Fall Time		---	5.2	---	
C _{iss}	Input Capacitance	$V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	463	---	pF
C _{oss}	Output Capacitance		---	82	---	
Crss	Reverse Transfer Capacitance		---	68	---	
I _s	Continuous Source Current ^{1,6}	$V_G=V_D=0\text{V}$, Force Current	---	---	-4	A
ISM	Pulsed Source Current ^{2,6}		---	---	-24	A
VSD	Diode Forward Voltage ²	$V_{GS}=0\text{V}$, $I_S=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

- 1、The data tested by surface mounted on a 1 inch FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3、The power dissipation is limited by 150°C junction temperature
- 4、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

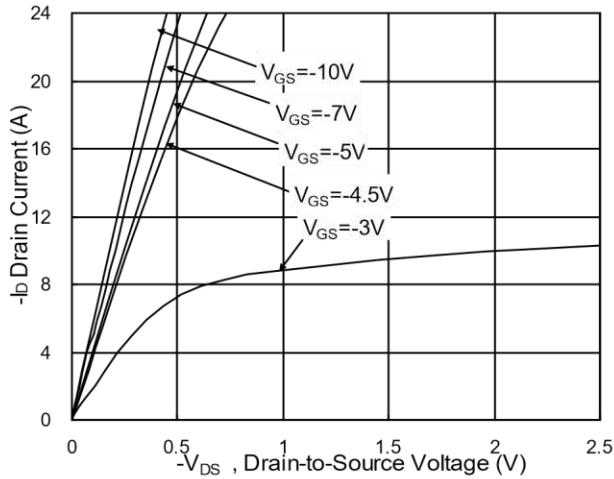


Fig.1 Typical Output Characteristics

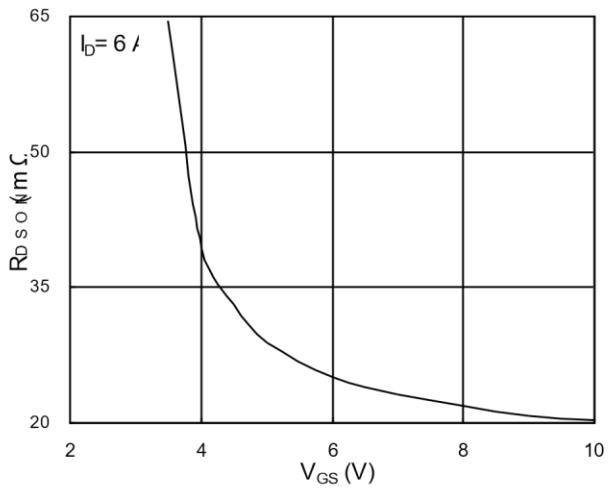


Fig.2 On-Resistance vs. Gate-Source

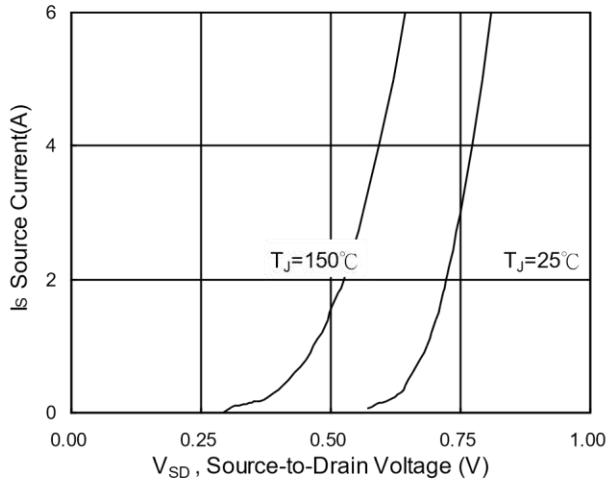


Fig.3 Forward Characteristics Of Reverse

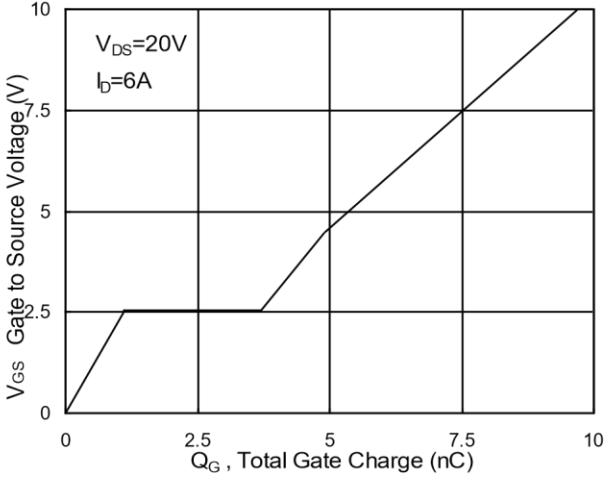


Fig.4 Gate-Charge Characteristics

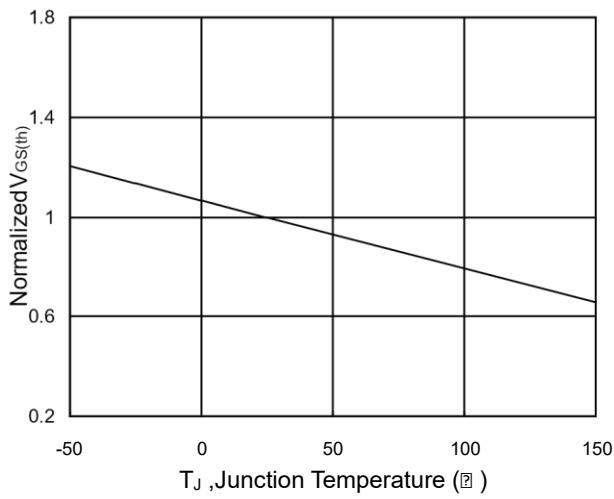


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

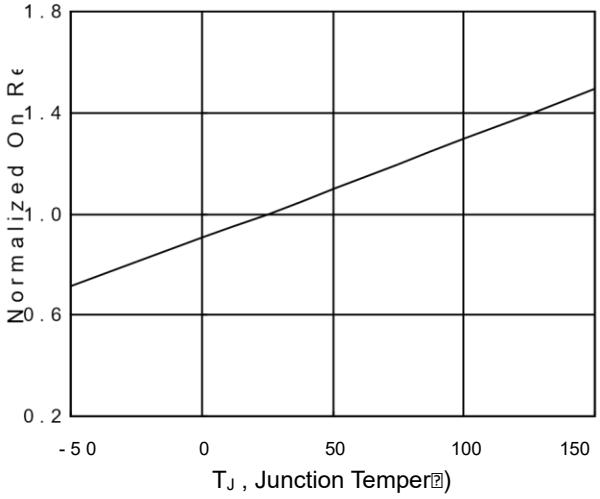
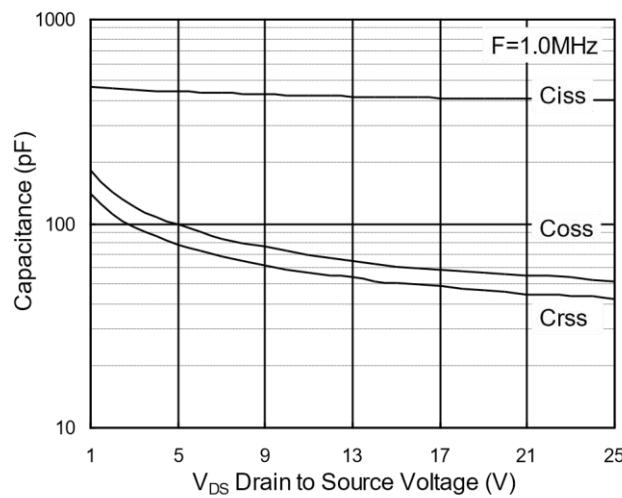
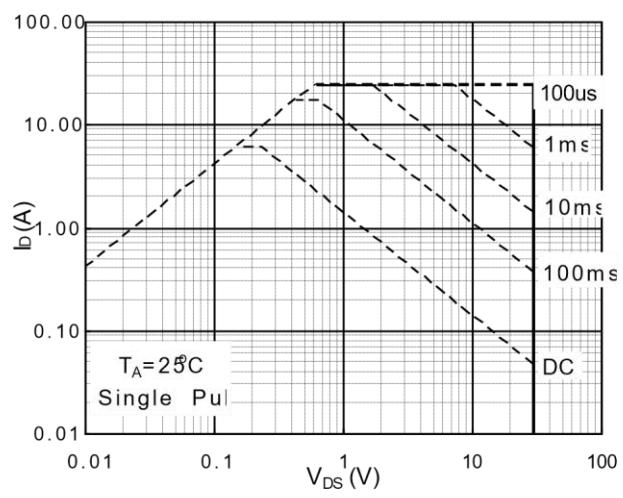
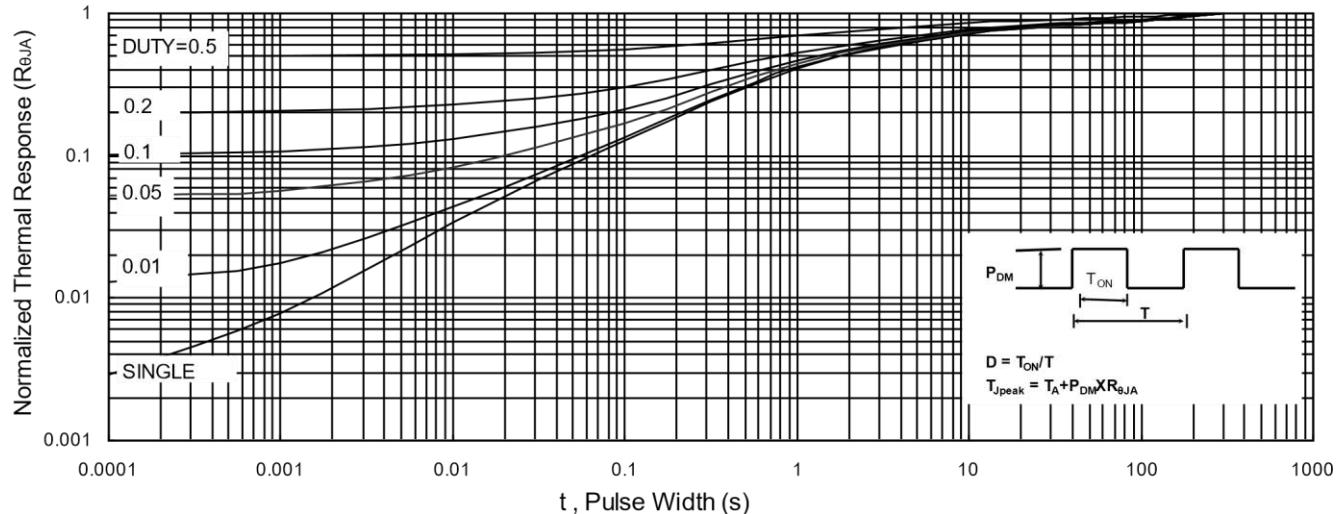
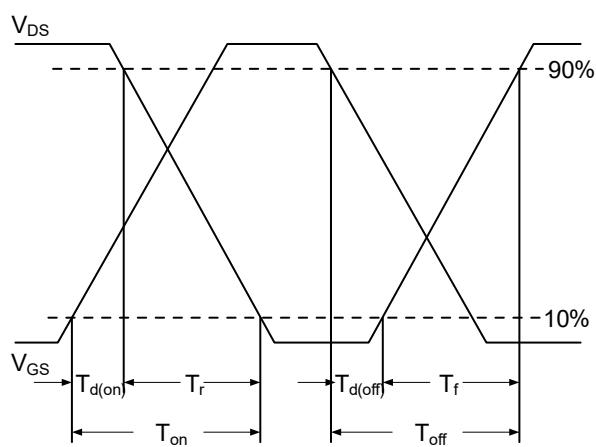
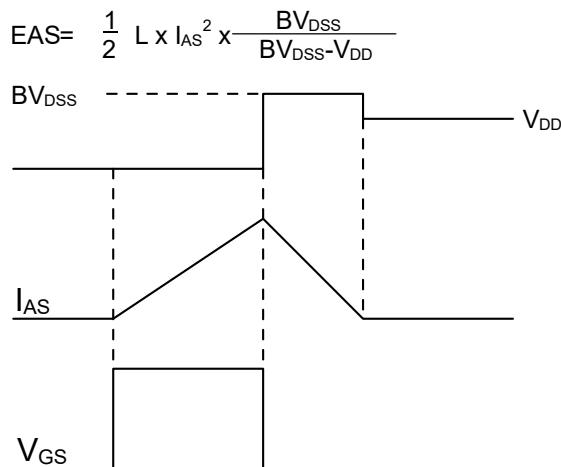


Fig.6 Normalized R_{DSON} vs. T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

P-Channel Typical Characteristics

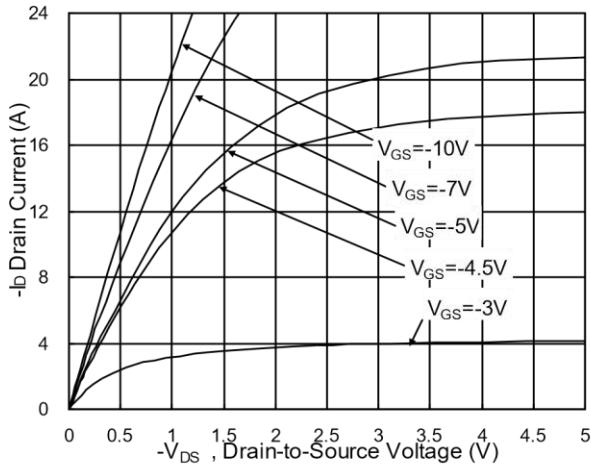


Fig.1 Typical Output Characteristics

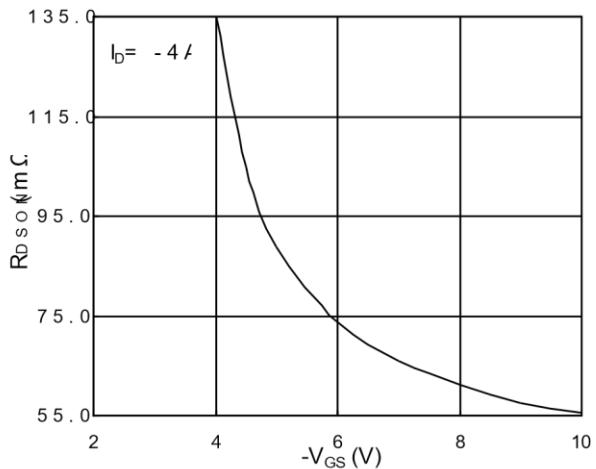


Fig.2 On-Resistance vs. G-S Voltage

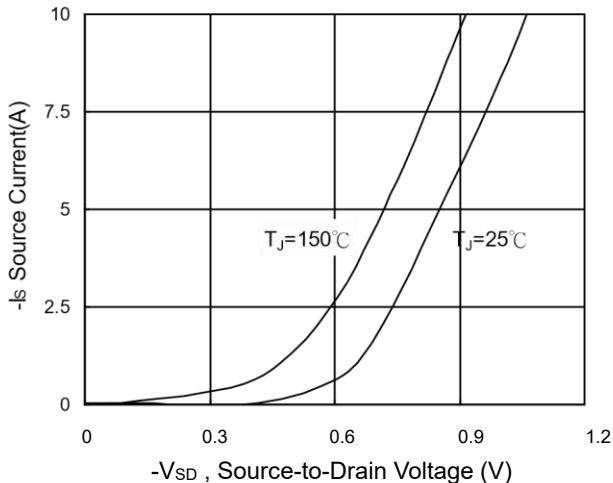


Fig.3 Forward Characteristics of Reverse

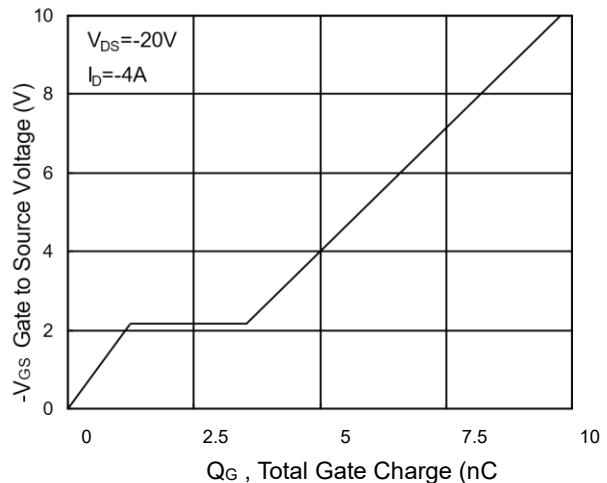


Fig.4 Gate-Charge Characteristics

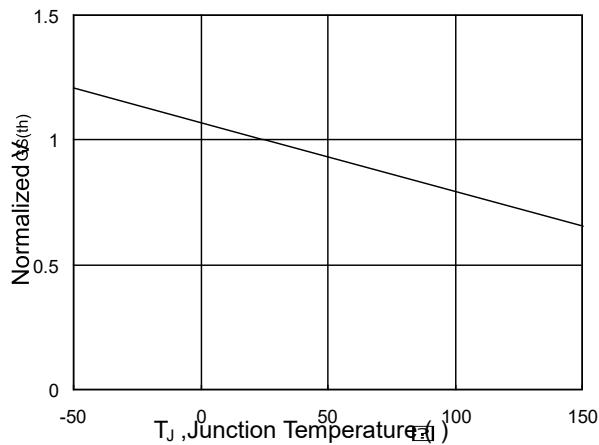


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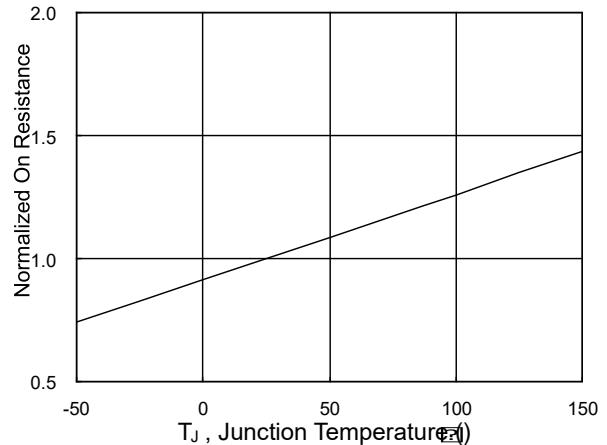
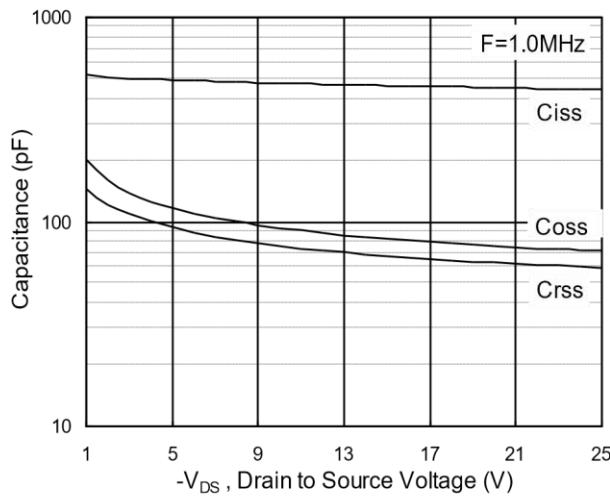
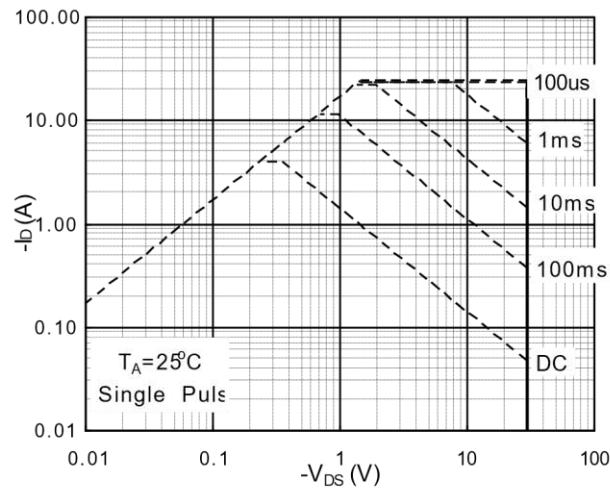
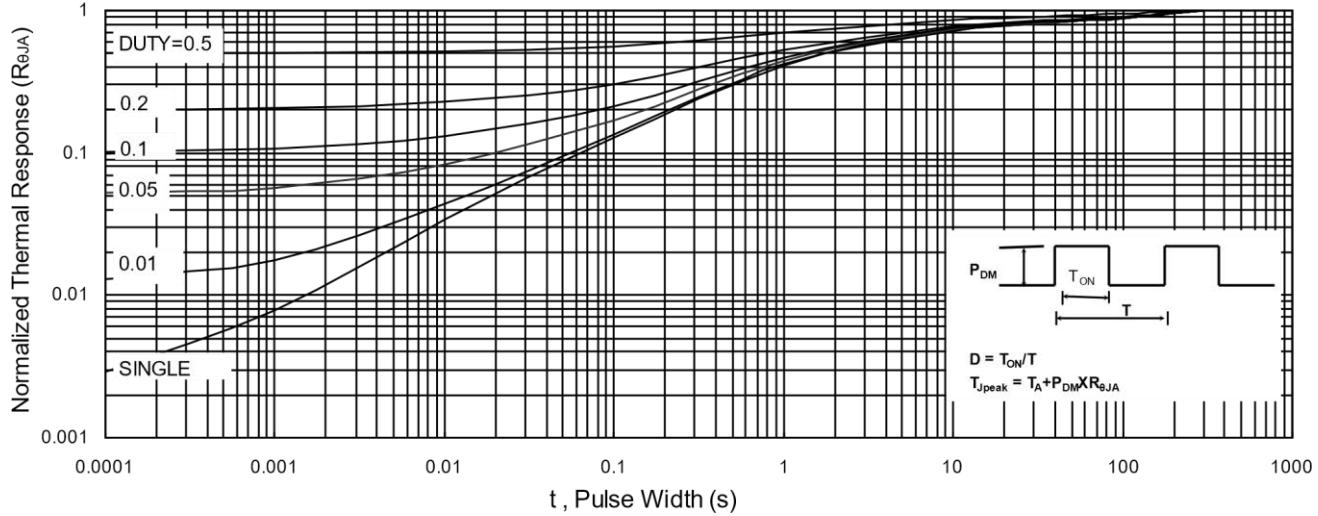
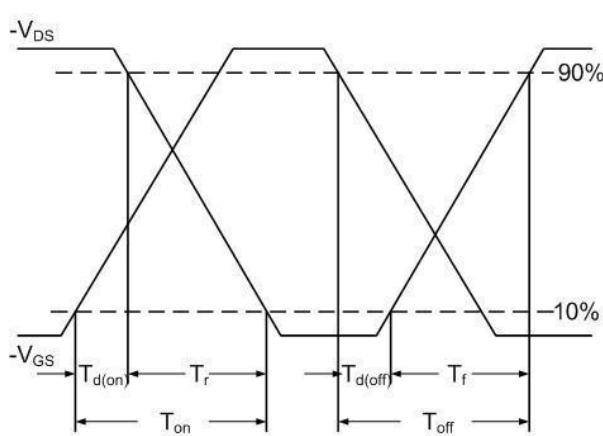
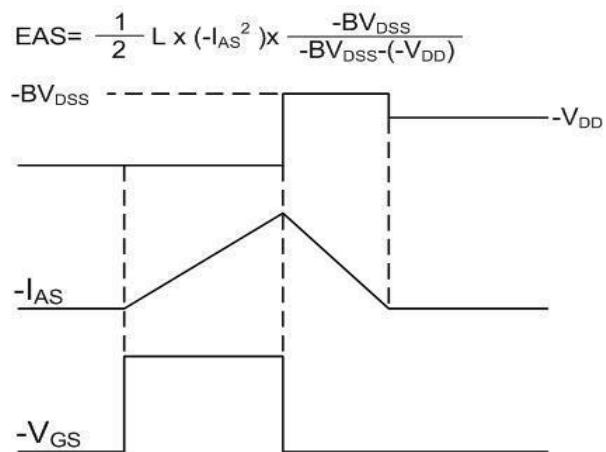
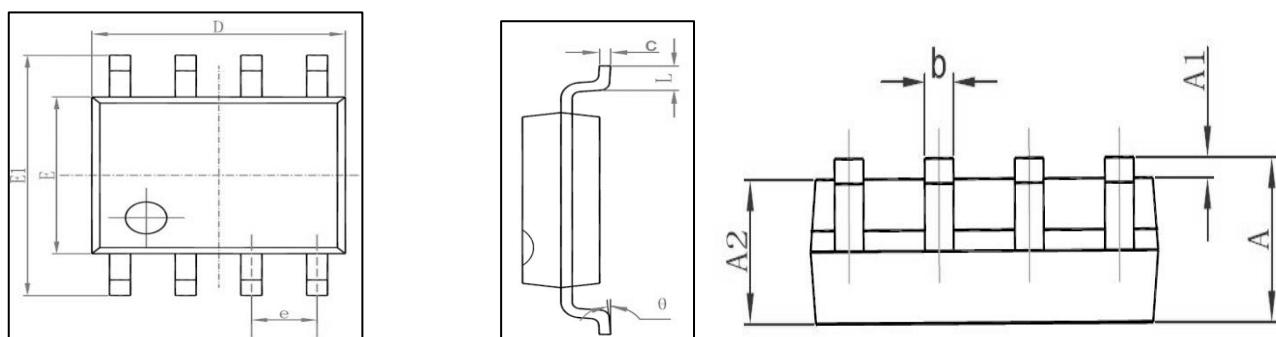


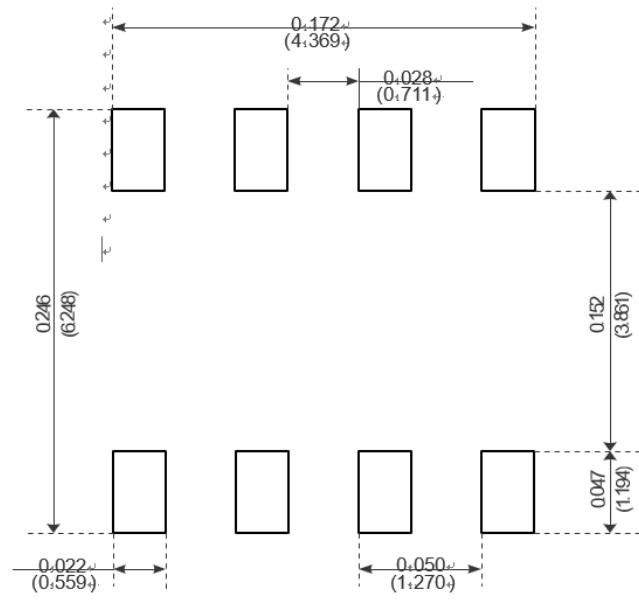
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Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads

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